



5,2 Vd:16MHz  
 1 Vd :9MHz  
 0.5 Vd:5MHz

ADC Max VINp-p 800mV (Normally)  
 Oscilloscope display Fullscale 720mV

3.3V 10KHz Square wave  
 PROBE\_COMP\_HOLE  
 PROBE\_GND\_PAD

DAC VREF  
 DAC\_OUT Fullscale Torrerant +-2%  
 ADC DC Resistance is 5k Ohm  
 ADCHS\_VP050  
 $(1.5/3.7) * 1.235 = 0.506V$

DAC OUT 0V to 1.235V  
 REAL 2mV to 1.234V  
 BIAS voltage adjust  
 CENTER = 0.25V  
 DAC OUT = 1.85 then 1.485V  
 R33:R34 = 1:4.1  
 DIVIDER = 80.39%  
 TOP 0.750V  
 BOTTOM -0.242V

EEPROM

Micro USB input

Negative voltage charge pump

BOOT select resistors  
 (LPC4370 pins are default weak pull up)

U1D  
 LPC4370FET100

XG2	P0_0	PF_4	H4	X
XG1	P0_1			
XH1	P1_0	P2_0	G10	X
XK2	P1_1	P2_1	G7	X
XK1	P1_2	P2_2	F5	X
XJ1	P1_3	P2_3	D8	X
XJ2	P1_4	P2_4	D10	X
XJ4	P1_5	P2_5	D9	X
XK4	P1_6	P2_6	G9	X
XG4	P1_7	P2_7	J5P	X
XH5	P1_8	P2_8	C4	X
XJ5	P1_9	P2_9	B10	X
XH6	P1_10	P2_10	E8	X
XJ7	P1_11	P2_11	A9	X
XK7	P1_12	P2_12	B9	X
XH8	P1_13	P2_13	A10	X
XH9	P1_14			
XK8	P1_15			
XH10	P1_16			
XJ10	P1_17			
XK9	P1_18			
XH11	P1_19			
XK10	P1_20			
XH7	P6_0	P3_0	A8	X
XG5	P6_1	P3_1	F2	X
XJ9	P6_2	P3_2	G6	X
XK6	P6_3	P3_3	A7	X
XJ6	P6_4	P3_4	B7	X
XK5	P6_5	P3_5	B2	X
XJ5	P6_6	P3_6	C2	X
XK9	P6_7	P3_7	D7	X
XK7	P6_8	P3_8	D7	X
XK9	P6_9	P3_8	E7	X
XK9	P6_11	P3_8	F7	X